

Fig. 1

FIG. 2 is a block diagram of a receiver system 201.

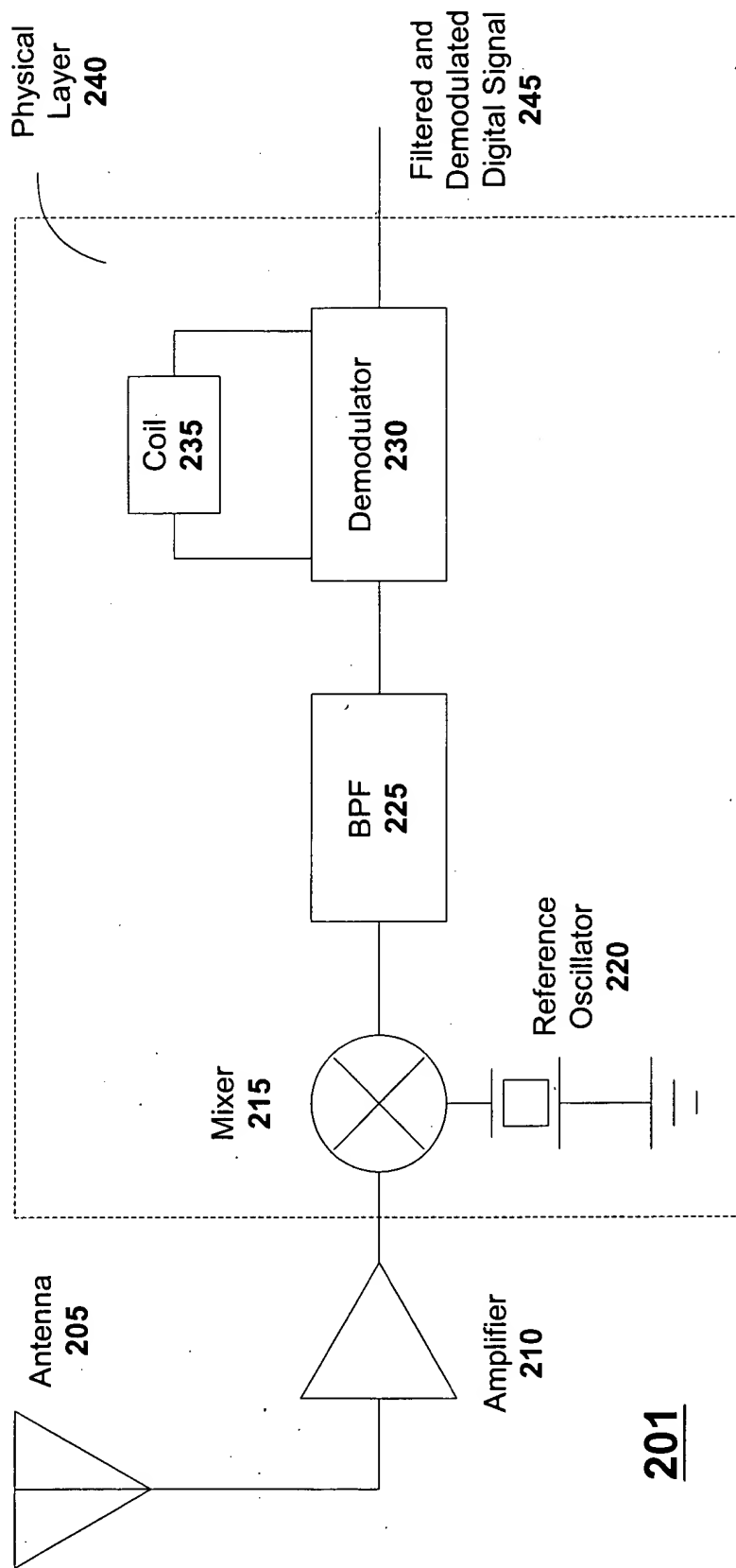


Fig. 2

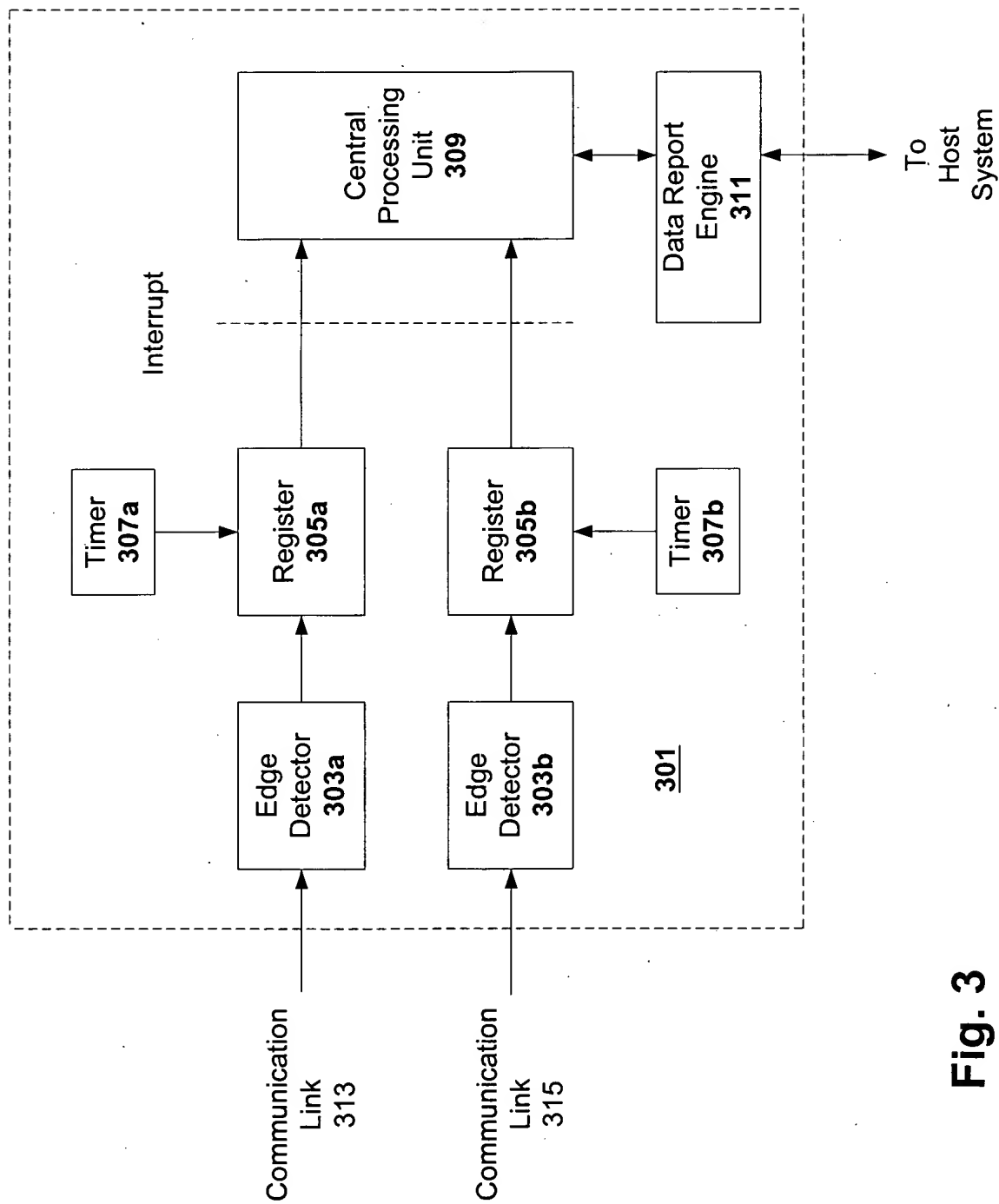


Fig. 3

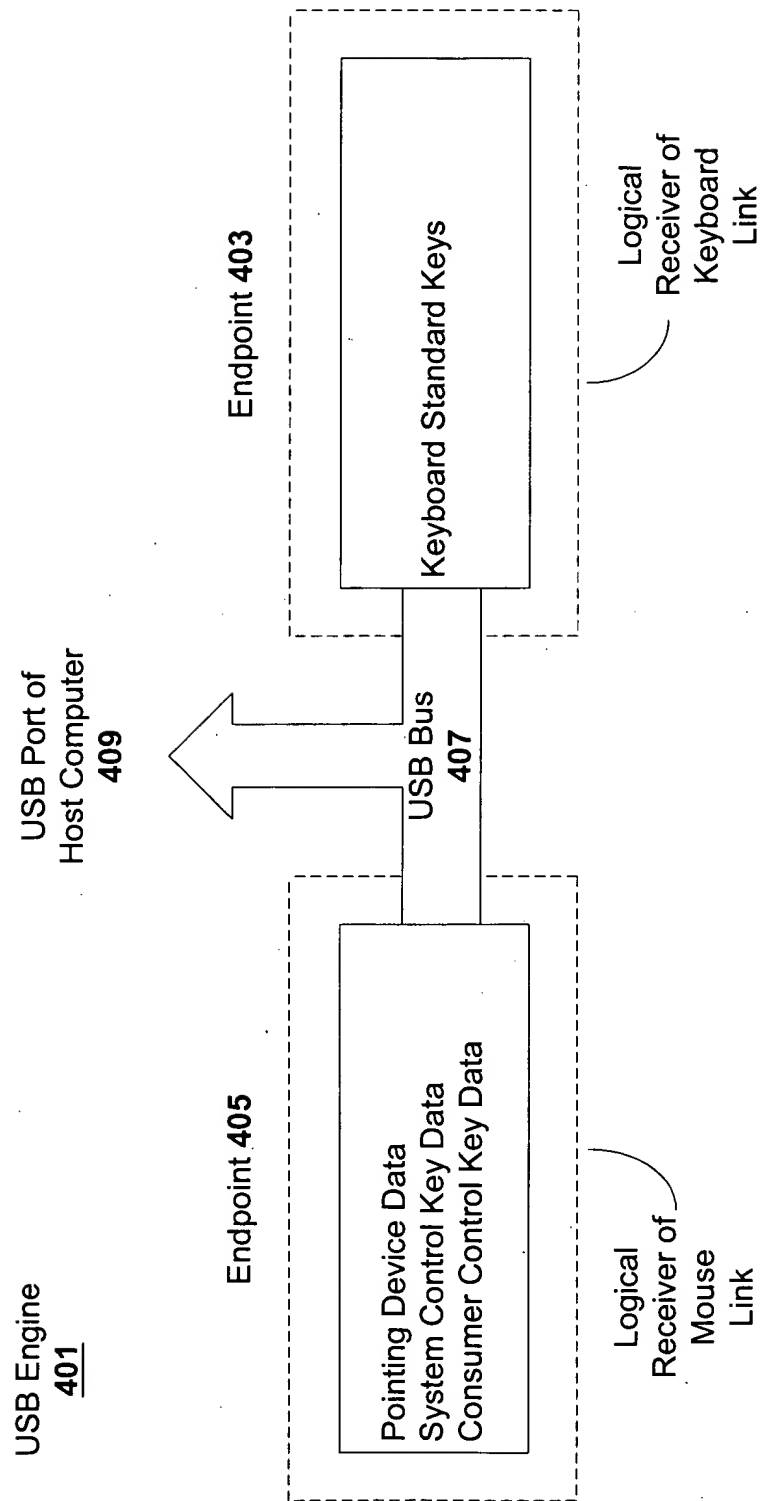


Fig. 4

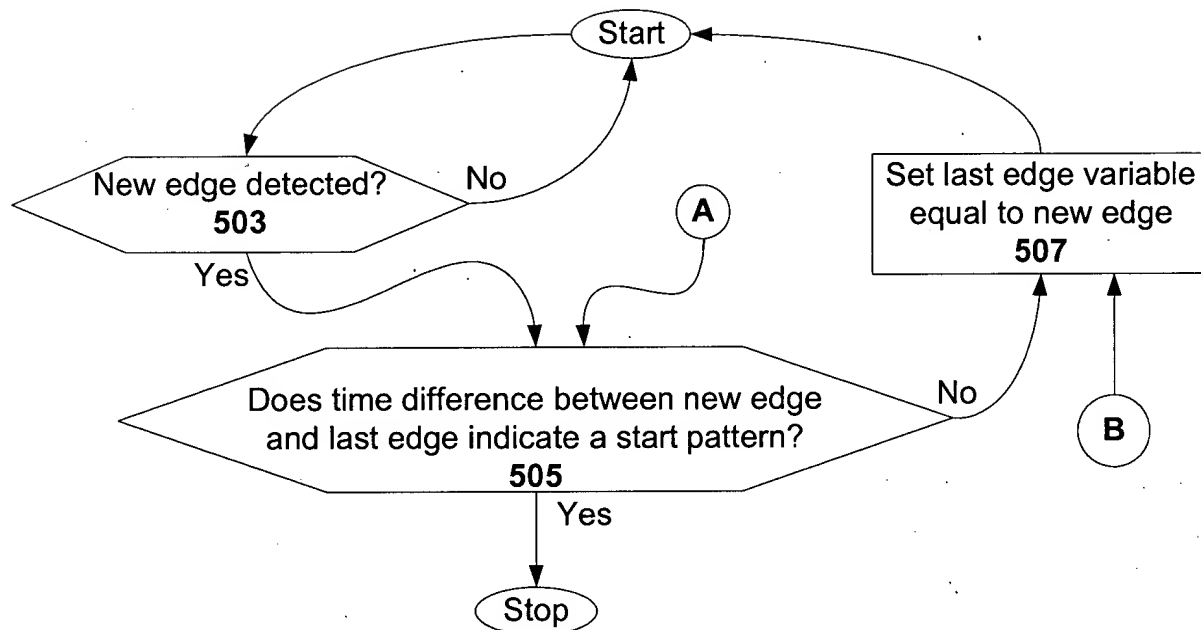


Fig. 5a

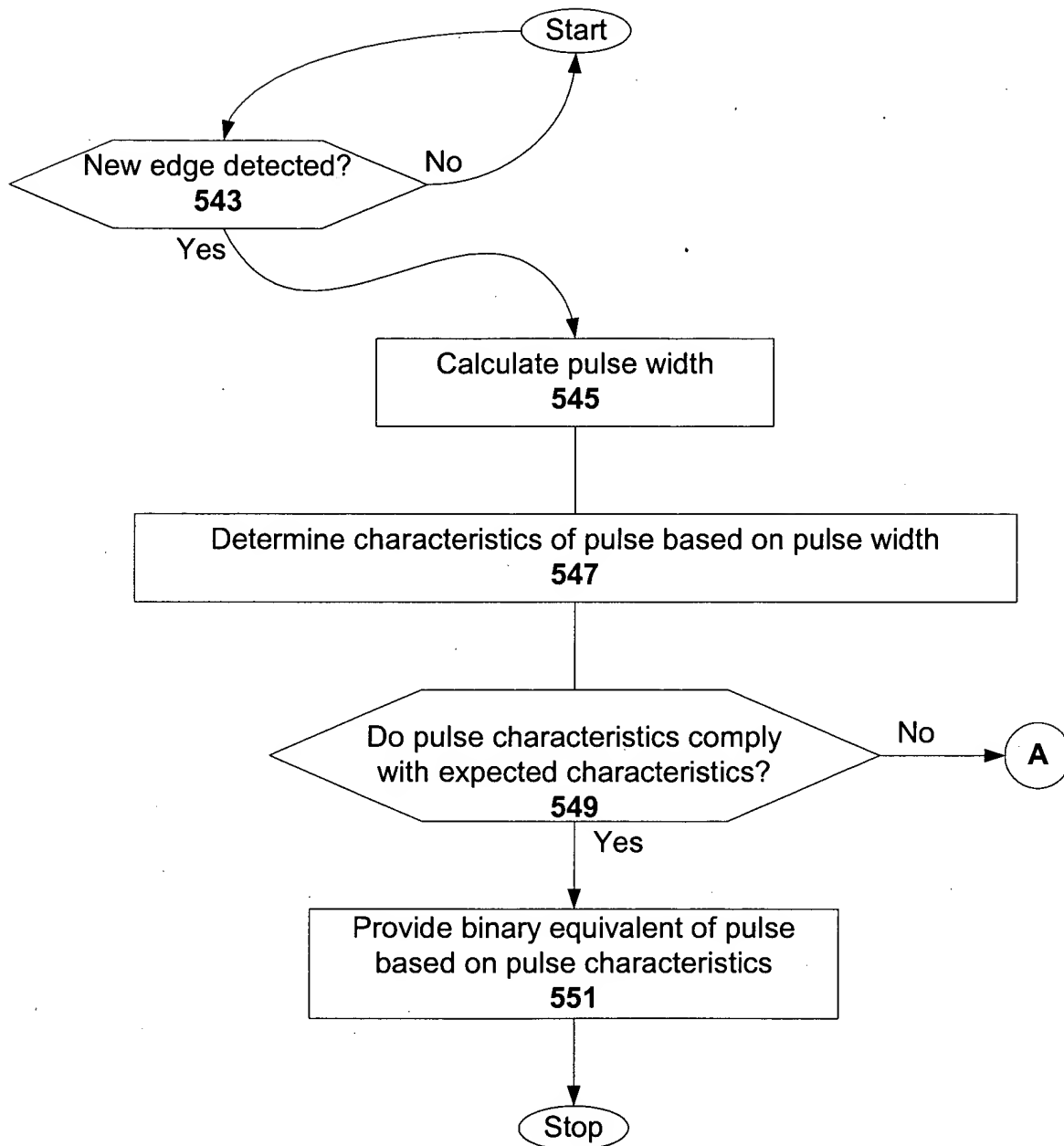


Fig. 5b

Fig. 6

